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1 Archaeologies of Memory: An Introduction

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physically isolated); the gateway validates all writes to memory. For the second problem, the encryption mechanism at the gateway will detect invalid data. For the third problem, we can use a heartbeat (separately clocked timer) at the gateway to detect denial of service from the execution processor. Now consider a foundry compromise of the ...

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Oscillations in the prefrontal cortex: a gateway to memory

tial memory beyond serving as a gateway to the hippocampus. INTRODUCTION Important contributions to our understanding of human memory come from the study of brain-damaged patients whose etiologies differ widely, including cerebro-vascular damage (infarct), progressive diseases (such as Alzheimerâ€™s and Parkin-

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KIBRA: A New Gateway to Learning and Memory?

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memory consolidation and hippocampal-cortical dialogue” will be associated with excessive activity in the medial temporal lobe during learning and, consequently, to overgeneralization ... Gateway to Memory: An Introduction to Neural Network Models of the Hippocampus and Learning. Cambridge, MA: MIT Press.

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the same internal memory. The networks read and write data to memory locations that have been specified in Anybus Configuration Manager - X-gateway. These memory locations are then exchanged between the networks. The data exchange model is different depending on if the X-gateway has a master/slave or slave/slave configuration.

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