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Fundamentals of digital logic with Verilog design / Stephen D. Brown, Zvonko G. Vranesic.â€”1st ed. p. cm.

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• NMOS and CMOS Logic Gates • Implementation of Logic Functions • Number Presentation and Arithmetic Circuits • Combinational Circuits • Sequential Circuits • Digital Circuits Design Required Texts & Materials Fundamentals of Digital Logic with Verilog Design, Stephen Brown and Zvonko Vranesic, McGraw Hill Publishing, 2008.

Fundamentals of Digital Logic with Verilog Design, Stephen

An arithmetic logic unit (ALU) is a digital circuit which performs arithmetic and logic operations on two n-bit digital words. The value of n can be 4, 8, 16, 32, or 64. Typical operations performed by the ALU are addition, subtraction, ANDing, ORing, and comparison of two n-bit digital words.

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